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Appl. No. 10/603,361 Paper filed 02/28/2008 FEB 2 8 2008 Attorney Docket No.: N1085-00089

TSMC 2002-0917

Suppl. Resp. to Office action of 10/30/2007

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Bor- Wen Chan, et al.

Examiner: Ron Everett Pompey

Serial No.: 10/603,361

Group Art Unit: 2812

Filed: 06/25/2003

Confirmation No.: 2523

For: METHOD OF MAKING A MULTIPLE GATE ELECTRODE ON A

SEMICONDUCTOR DEVICE

### CERTIFICATE OF MAILING/FACSIMILE TRANSMISSION PURSUANT TO 37 C.F.R. §1.8

I hereby certify that this correspondence (and anything referred to as being transmitted herewith) is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571-273-8300) on the date shown below.

Date: February 28, 2008

Maria E. Provencio

Examiner Ron Everett Pompey Mail Stop AF / Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## SUPPLEMENTAL RESPONSE TO OFFICE ACTION DATED OCTOBER 30, 2007 REQUEST FOR RECONSIDERATION

#### Commissioner:

Responsive to the final Office action dated October 30, 2007, the Advisory Action dated January 17, 2008, and the telephone conversation with Examiner Pompey that took place on January 31, 2008, and further to Applicants' previously-filed Response of December 27, 2007, please re-examine and reconsider the subject patent application according to the remarks provided.

No claim amendments are filed herein. A listing of claims is provided for the Examiner's benefit and begins on page 2 of this paper. Remarks/Arguments begin on page 6 of this paper.

This Supplemental Response is being filed along with a 1-Month Extension of Time and the appropriate fee.

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